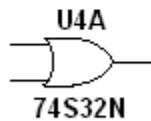
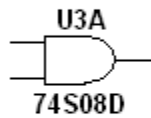
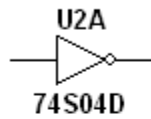
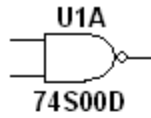


ET255 DIGITAL ELECTRONICS 1
HOMEWORK 7
PROBLEM SET

1. Draw the ANSI/IEEE Standard Symbol for the following logic gates



2. On page 79, Figure 9-3 in the lab manual, redraw the circuit schematic diagram using the Distinctive Symbols.

3. In the Digital Fundamentals Textbook, Chapter 5, Figure 5-2, redraw the circuit schematic diagram using the ANSI/IEEE Standard Symbols.

4. Draw the Logic Circuit for the following Boolean Expressions:

(a) $X = AB(CD' + EF)$

(b) $X = '(AB)C + AB'C$

(c) $X = AB + CD$

(d) $X = AB' + A'B$

5. What Logic Gate Function is being performed in Problem 4(a)?

6. Write a one page paper on how this Digital Electronics 1 course falls under the "Learn by Making" philosophy of the MAKE: Education Article passed out in class by the Instructor. Contrast Continuing Education via Training vs. Learn by Making within the paper.